



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yutaka Kobayashi

Serial No.: 09/893,791

Filed: June 29, 2001

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING SAME

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

MAR 18 2002

TECHNOLOGY CENTER 2800

Group Art Unit: 2815

Examiner: Chris C. Chu

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3-26-02

**RESPONSE TO FIRST OFFICE ACTION AND RESTRICTION
REQUIREMENT**

Sir:

In response to the Office Action dated February 20, 2002, please consider the following:

REMARKS

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group I (e.g., drawn to a semiconductor device as defined by claims 1-4), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Further, Applicant respectfully requests the Examiner's acknowledgment of the priority document filed on June 29, 2001.

Early, favorable prosecution on the merits is respectfully requested.

Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.

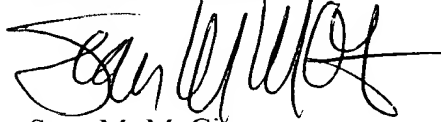
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A conditional petition is made for any extension of time which may become necessary.
The Commissioner is authorized to charge any fees for such extension and to credit any
overpayment in fees to Attorney's Deposit Account No. 50-0481.

Respectfully Submitted,

A handwritten signature in black ink, appearing to read "Sean McGinn", written over a horizontal line.

Sean M. McGinn
Reg. No. 34,386

Date: March 15, 2002
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